U-Boot & Linux Kernel Board Port

In this session we will cover fundamentals necessary to port a TI Linux-based EVM platform to a custom target platform. We will introduce the necessary steps needed to port the following components: secondary program loader, u-boot and Linux kernel.


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Pre-work Check List

- Installed and configured VMWare Player v4 or later
- Installed Ubuntu 10.04
- Installed the latest Sitara Linux SDK and CCSv5
- Within the Sitara Linux SDK, ran the setup.sh (to install required host packages)
- Using a Sitara EVM, followed the QSG to connect ethernet, serial cables, SD card and 5V power
- Booted the EVM and noticed the Matrix GUI application launcher on the LCD
- Pulled the ipaddr of your EVM and ran remote Matrix using a web browser
- Brought the USB to Serial cable you confirmed on your setup (preferable)
Agenda

- Board Port Overview
- Porting U-Boot to an AM335x Target
- U-Boot Board Port Labs
- Porting the Linux Kernel to a AM335x Target
- Linux Kernel Board Port Labs
Board Port Overview
Presentation Overview

• Goal is to gain an understanding of the components of a board port for both U-Boot and Linux

• The board or target portion is the last part of a three step method (Architecture/SOC/Target Board)

• Explain how the SDK will support board ports going forward
Linux Board Background Assumptions

• Already Familiar with:
  – SPL/U-Boot/Linux (😊)
  – SPL/U-Boot/Linux boot sequence
  – U-Boot/Linux build process (kernel configuration)
  – Minicom setup
  – Root File Systems

• Very limited time,
  – Really only have time to show the tip of the iceberg, not going to all inclusive or discuss every facet of board porting, this is a starting place
  – we’ll have to take extended question/answer after the class in the foyer or later over email. (or in the bar…. You buy 😊)

• This information is good for today only………. always in flux…..

• What’s presented here today may not be the only way of implementation

• Standard disclaimer of “You can and should use what others have done as a method on what to do to move forward”
Things not covered today..

- Not covering all of the board port steps
  - Limited time today, so we will just be focusing on the code portion of the port
  - Directory setup
  - Machine ID discussion
  - Makefile modifications
  - Git Setup
  - Other Processors
Linux Board Port Workshop Agenda

• The Mission/ “So…what’s a board port?”

• Look at the System Block Diagram of the target board being used

• Stages of a port

• Pin Mux Utility Tool Overview

• U-Boot Port
  – source tree
  – introduce the target board file
  – Perform two labs that use an already ported example (the code added by with each lab will be discussed)

• Linux Kernel Port
  – source tree
  – introduce the target board file
  – Perform four labs that use an already ported example (the source additions for each lab will be discussed)
The Mission

“Good Morning … the AM335x has been chosen as the processor for your new exciting market cornering product. Your job (no choice but to accept it 😊) is to get U-Boot and the Linux kernel running on this new platform as soon as possible.

To accomplish this you will take the board design from your HW team and use the AM335x EVM and accompanying Sitara Linux SDK and port U-Boot and the Linux kernel to your new Hardware.”
So….What’s a board port?

- It is taking the Sitara Linux SDK that is working on a known platform and moving it to a new target platform that is based on the same TI AM335x processor.
Target Board for this Exercise…. Beagle Bone
Target Board Port Configuration Example

- 5VDC
- USB
- System Power TPS65217
- RST Button
- 2 Port USB HUB
- FT2232H USB To Serial
- UART0
- AM335x
- DDR
  - DDR2 1x16 256MB
- MI
  - 10/100 Ethernet Phy
  - RJ45
- USB1
- MMC0
- uSD
Will be adding an LCD to the system....
Board Port.... Tip of the iceberg

Used to show the balance of work necessary
A Tale of Two Board Files

- Both U-boot and Linux follow a similar board file abstraction approach
- The Core Architecture is ported first
- The SOC supporting functions are ported next
- The last part to tie U-Boot/Kernel to the target is the Board file that defines “well known” initialization or entry functions that U-Boot and the Linux Kernel will call to handle “a priori” type board knowledge
Where the U-boot and Kernel Sources are after TI-SDK-AM335x-05.04.01.00 installation

• Both the U-Boot and the Linux Kernel Sources are found in the installed TI-SDK-AM335x-05.04.01.00 directory

• Later in the presentation you will see references to just the specific sub-tree that has the respective source such as U-Boot or Linux
• GPIO Signals are “muxed” with peripheral interfaces. These can be configured into one of several modes either supporting the peripheral or remaining in a GPIO mode.
Selecting a mode using Pin Mux Utility

- Each Pin has a mode selection, using UART0 as an example here
- UART0 RXD Mode 0 is selected and GPIO 1.9 is de-selected, notice Pad config changed too.

<table>
<thead>
<tr>
<th>Pad Config.</th>
<th>I/O Power</th>
<th>Node 0</th>
<th>Mode 1</th>
<th>Mode 2</th>
<th>Mode 3</th>
<th>Mode 4</th>
<th>Mode 5</th>
<th>Mode 6</th>
<th>Mode 7</th>
</tr>
</thead>
<tbody>
<tr>
<td>IO IEN OFF</td>
<td>E15 /-</td>
<td>VDD DVH=3.3V</td>
<td>UART0 TXD</td>
<td>SPI1 CS0 M..</td>
<td>DCDAN TX M..</td>
<td>IO2 D SDA M..</td>
<td>ECP2 IN P..</td>
<td>PR1 PRU1 P..</td>
<td>PR1 PRU1 P..</td>
</tr>
<tr>
<td>IO IEN OFF</td>
<td>E16 /-</td>
<td>VDD DVH=3.3V</td>
<td>UART0 TXD</td>
<td>SPI1 CS1 M..</td>
<td>DCDAN RX M..</td>
<td>IO2 SCL M..</td>
<td>ECP1 IN P..</td>
<td>PR1 PRU1 P..</td>
<td>PR1 PRU1 P..</td>
</tr>
</tbody>
</table>

- Utility helps find conflicts, two pins are simultaneously selected
- Each Pin has a mode selection, using UART0 as an example here

- Pin Mux Utility User Guide
Porting U-Boot to an AM335x Target
U-Boot Port Agenda

• What are the different stages of a Port
• Introduce the board file, where it fits in the Port Picture, where it is in the source tree
• What is the anatomy of the board file
• Introduce the Board File Template that can be used to port u-boot
• Labs Introduction
U-Boot Board Port Exercises and Source Links

• Link to the U-Boot Labs

• Link to the U-Boot Template Source tree (clone this tree)
  – [git://gitorious.org/sitara-board-port/sitara-board-port-uboot.git](http://arago-project.org/git/projects/?p=u-boot-am33x.git;a=summary)

• PSP U-boot Repo
  – [http://arago-project.org/git/projects/?p=u-boot-am33x.git;a=summary](http://arago-project.org/git/projects/?p=u-boot-am33x.git;a=summary)
SPL and U-Boot Builds

• “Dude…… Where’s my X-Loader?”
  • It has left the building…. Been replaced by SPL

• The same code base is used to build U-Boot (u-boot.img) and the SPL (still called MLO). Since the same code base is used pre-processor flags are used to isolate the code between the two builds. For example, you do not want the DDR and MPU clock init code in both builds. Also of merit is that one build yields both images.

• Below are examples of the pre-processor flags used:

```c
#ifdef CONFIG_SPL_BUILD
#endif
#endif
```
U-Boot Source Directory

- Using the existing am335x source directory
- The developer will be concentrating on one source directory and for the most part one include directory
U-Boot Anatomy of a board File

- Defines Required interface functions for SPL and U-Boot
- One source file contains the code for both SPL and U-Boot and are separated by pre-processor flags
- SPL handles the initialization of clocks, DDR, Serial Port and PMIC
- Some functions are defined twice in both an SPL context and then again in a U-Boot context (s_init & board_init)
- The board file is where the developer will spend most of their effort for a port
U-Boot/SPL Board Template File

- The board file (evm.c) used here today is different from the one provided in the SDK.
- Contains the code for both SPL and U-Boot.
- This Board Template only enables MPU Clock, DDR and the Serial Port.
- It’s up to developer to decide how much functionality they choose to put into the board file and hence the u-boot.img. If the target board supports more peripherals but only one or two is needed to boot into the kernel why add that code?
U-Boot Board Port Labs
Board Port Labs

• Lab 1
  – Introduce the template board file and how SPL and u-boot.img are built

• Lab 2
  – Build on the template file demonstrating how to add the MMC and Ethernet peripherals
Board Port Source Tree being used

- Currently Source is derived from AM-SDK-05.04.01.00, the Port Tree will follow or track each SDK release
- A git tree has been set up for these labs on the host machines
- Using existing board file name and build methods
- Using the default U-Boot configuration supplied with the SDK

Lab Git Tree tag progression tags are SDK <version>-<modification>

Base
board-evm.c

Create Template Board file
05.04.01.00-template

MMC enable
05.04.01.00-mmc

Ethernet enable
05.04.01.00-ethernet
U-Boot Board Port Exercise 1 - Overview

- **Goal**: Introduce workshop attendees to a board template file that can be used later for a U-Boot Board port

- **How this is Demonstrated**
  - Build both an SPL and u-boot.img using provided AM335x board template file, which has:
    - Base processor configuration for u-boot, ddr, clocks and a serial console are initialized

- **What is being done**:
  - Examine the board file to see what is being initialized

- **Perform the Lab**
First Burning Question:
So... where are the DDR timings and the Clock set?
First Burning Question: So... where are the clock and DDR timings set? DDR First

- DDR Setup requires portions of 4 functional blocks to be setup. (Block Diagram)
- EMIF, CMD, DATA and EMIF0 CLK are dependent on Memory selected
First Burning Question: So... where are the clock and DDR timings set? DDR First

- The DDR is set up within the SPL context
- `enable_ddr_clocks` in `pll.c`
- `ddr_defsh.h` and `cpu.h`
Here is link to a Tool that can be used to generate necessary values to configure DDR

- Spread Sheet Tool can be found here
The SPL entry function

- s_init is called from lowlevel_init.S to setup system PLL, RTC, UART, timer and finally configures DDR

```c
/* early system init of muxing and clocks. */

void s_init(void)
{
  / u-boot context /

#ifdefined CONFIG_SPL_BUILD
  /* Setup the PLLs and the clocks for the peripherals */
  pll_init();
  /* Enable RTC32K clock */
  rtc32k_enable();
  /* UARTs of timeset */
  enable_uart0_pin_mux();

  /* Disable smart idle */
  /* Initialize the Timer */
  init_timer();
  preloader_console_init();
  config_am335x_ddr();
#endif

board/ti/am335x/ovm.c

This function has both SPL and u-boot contexts
```
And now to Set the MPU Clock Rate....

- SPL Context Function
- Before setting the MPU PLL, the voltage and current are increased using I2C commands to the tps65217.

```c
void spl_board_init(void)
{
    enable_i2c0_pin_mux();
    i2c_init(,);

    /* BeagleBone PMIC Code */
    i2c_probe(TPS65217_CHIP_PM)

    /* Increase USB current limit to 1300mA */
    tps65217_reg_write(, ,USB_INPUT_CUR_LIMIT_1300MA,
                     USB_INPUT_CUR_LIMIT_MASK)

    /* Set DCDC2 (MPU) voltage to 1.275V */
    tps65217_voltage_update(,DCDC_VOLT_SEL_1275MV)

    /* Set LDO3, LDO4 output voltage to 3.3V */
    tps65217_reg_write(,,,LDO_VOLTAGE_OUT_3_3,)

    /* Set MPU Frequency to 720MHz */
    mpu_pll_config(MPUPLL_M_720);
}
```

- Called from arch/arm/cpu/armv7/start.S
- If you have a different PMIC you will most likely need a different code base than what is shown here
Board File Template for u-boot.img

- Within the u-boot context this is the entry function
- Same source file as used for SPL
- Pin Mux config is setup for i2c, uart (already done in SPL) and

```c
int board_init(void)
{
    /* Configure the i2c0 pin mux */
    enable_i2c0_pin_mux();

    i2c_init(CONFIG_SYS_I2C_SPEED, CONFIG_SYS_I2C_SLAVE);

    board_id = BONE_BOARD;
    configure_evm_pin_mux(board_id);

    #ifdef CONFIG_SPL_BUILD
    board_evm_init();
    #endif

    pmic_init();

    return 0;
}
```

board/ti/am335x/evm.c
Do LAB 1........
U-Boot Board Port Exercise 2 - Overview

• Goal: Take the board template file (evm.c) and add both MMC and Ethernet support

• How this is Demonstrated
  – Using the supplied git tree checkout a Ethernet tagged branch, this has both the MMC and Ethernet support code. Build the kernel.
    • This adds Pin Mux support for both Ethernet and MMC
    • Adds the init functions for Ethernet and MMC.

• What is being done:
  – Examine the code changes necessary to implement Ethernet and MMC

• Perform the Lab
Steps to adding MMC and Ethernet to the target board file

- Review system info to see how peripheral is attached
- Pin Mux
  - Use the Pin Mux Utility to configure Pin Init data
- Create Device Init function
  - If device is supported in U-Boot, set the desired include in include/configs
- Add Device Init Function to board file
### Pin Mux Utility

- Pin Mux tool capture for MII interface
- While the tool shows GMII, this is the MII interface, doc bug in tool

```c
static struct module_pin_mux mii1_pin_mux[] = {
    {OFFSET(mii1_rxerr), MODE(0) | RXACTIVE}, /* MII1_RXERR */
    {OFFSET(mii1_txen), MODE(0)},                   /* MII1_TXEN */
    {OFFSET(mii1_rxdv), MODE(0) | RXACTIVE},        /* MII1_RXDV */
    {OFFSET(mii1_txd3), MODE(0)},                   /* MII1_TXD3 */
    {OFFSET(mii1_txd2), MODE(0)},                   /* MII1_TXD2 */
    {OFFSET(mii1_txd1), MODE(0)},                   /* MII1_TXD1 */
    {OFFSET(mii1_txd0), MODE(0)},                   /* MII1_TXD0 */
    {OFFSET(mii1_txclk), MODE(0) | RXACTIVE},       /* MII1_TXCLK */
    {OFFSET(mii1_rxclk), MODE(0) | RXACTIVE},       /* MII1_RXCLK */
    {OFFSET(mii1_rxd3), MODE(0) | RXACTIVE},        /* MII1_RXD3 */
    {OFFSET(mii1_rxd2), MODE(0) | RXACTIVE},        /* MII1_RXD2 */
    {OFFSET(mii1_rxd1), MODE(0) | RXACTIVE},        /* MII1_RXD1 */
    {OFFSET(mii1_rxd0), MODE(0) | RXACTIVE},        /* MII1_RXD0 */
    {OFFSET(mdio_data), MODE(0) | RXACTIVE | PULLUP_EN}, /* MDIO_DATA */
    {OFFSET(mdio_clk), MODE(0) | PULLUP_EN},         /* MDIO_CLK */
    {-1},
};
```

<table>
<thead>
<tr>
<th>Pin Config</th>
<th>Boll/Top Ball</th>
<th>IO Power</th>
<th>Mode 0</th>
<th>Mode 1</th>
<th>Mode 2</th>
<th>Mode 3</th>
<th>Mode 4</th>
<th>Mode 5</th>
<th>Mode 6</th>
<th>Mode 7</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>EN PD</td>
<td>J15 -</td>
<td>VDDS:5V=3.3V</td>
<td>MII1 RXER</td>
<td>RMII1 RXER</td>
<td>SPC1 D1 M...</td>
<td>IC21 SCL M...</td>
<td>MCSI1 F5...</td>
<td>UART1 TSM...</td>
<td>UART2 TXO...</td>
</tr>
<tr>
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<td>IDIS PD</td>
<td>J16 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 TXEN</td>
<td>RMII1 TXEN</td>
<td>RGMN1 TCTL</td>
<td>TIMER4 MD7</td>
<td>MCSI1 AX...</td>
<td>EGEPO IN1...</td>
<td>MCCI2 CMD...</td>
</tr>
<tr>
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<td>VDDS:5V=3.3V</td>
<td>MII1 RXDV</td>
<td>LCD MEM...</td>
<td>RGMN1 RCTL</td>
<td>UART5 TXD...</td>
<td>MCSI1 ACC...</td>
<td>MCCI2 DATO...</td>
<td>MCCI1 ACC...</td>
</tr>
<tr>
<td>0</td>
<td>IDIS PD</td>
<td>J18 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 TXD...</td>
<td>DCCAN0 TX...</td>
<td>RGMN1 TD3</td>
<td>UART4 RXD...</td>
<td>MCSI1 FS...</td>
<td>MCSD1 DAT...</td>
<td>MCCI1 FS...</td>
</tr>
<tr>
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<td>VDDS:5V=3.3V</td>
<td>GMI1 TXD...</td>
<td>DCCAN0 RX...</td>
<td>RGMN1 TD2</td>
<td>UART4 TXO...</td>
<td>MCSI1 AX...</td>
<td>MCCI2 DAT2...</td>
<td>MCCI1 AX...</td>
</tr>
<tr>
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<td>K16 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 TXD...</td>
<td>RMII1 TXD...</td>
<td>RGMN1 TD1</td>
<td>MCSD1 FS...</td>
<td>MCSI1 AX...</td>
<td>EGEPO IN1...</td>
<td>MCCI1 CMD...</td>
</tr>
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<td>K17 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 TXD...</td>
<td>RMII1 TXD...</td>
<td>RGMN1 TD0</td>
<td>MCSD1 AX...</td>
<td>MCSI1 ACC...</td>
<td>EGEPO IN1...</td>
<td>MCCI1 CLK...</td>
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<tr>
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<td>EN PD</td>
<td>K18 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 TXCLK</td>
<td>UART2 RXD...</td>
<td>RGMN1 TCLK</td>
<td>MCCI0 DAT7</td>
<td>MCCI1 DAT0...</td>
<td>UART1 DCO...</td>
<td>MCCI1 ACC...</td>
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<tr>
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<td>EN PD</td>
<td>L16 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 RXCLK</td>
<td>UART2 TXO...</td>
<td>RGMN1 RCLK</td>
<td>MAP1 DAT6</td>
<td>MCCI0 DAT...</td>
<td>UART1 RSS...</td>
<td>MCCI0 ACC...</td>
</tr>
<tr>
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<td>L17 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 RXD...</td>
<td>UART2 RXD...</td>
<td>RGMN1 RD3</td>
<td>MAP1 DAT5</td>
<td>MCCI0 DAT...</td>
<td>UART1 DTR...</td>
<td>MCCI0 ACC...</td>
</tr>
<tr>
<td>1</td>
<td>EN PD</td>
<td>L18 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 RXD...</td>
<td>UART2 RXD...</td>
<td>RGMN1 RD2</td>
<td>MAP1 DAT4</td>
<td>MCCI0 DAT...</td>
<td>UART1 RIN...</td>
<td>MCCI0 ACC...</td>
</tr>
<tr>
<td>1</td>
<td>EN PD</td>
<td>L19 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 RXD...</td>
<td>UART2 RXD...</td>
<td>RGMN1 RD1</td>
<td>MAP1 FS...</td>
<td>MCCI0 AX...</td>
<td>EGEPO STR...</td>
<td>MCSD1 CLK...</td>
</tr>
<tr>
<td>1</td>
<td>EN PD</td>
<td>M16 -</td>
<td>VDDS:5V=3.3V</td>
<td>GMI1 RXD...</td>
<td>UART2 RXD...</td>
<td>RGMN1 RD0</td>
<td>MAP1 FS...</td>
<td>MCCI0 AX...</td>
<td>MCCI0 ACC...</td>
<td>MCCI0 ACC...</td>
</tr>
<tr>
<td>1</td>
<td>IDIS PD</td>
<td>H18 -</td>
<td>VDDS:5V=3.3V</td>
<td>RMII RXFCD...</td>
<td>XDMA EVE...</td>
<td>SR1 C50 M...</td>
<td>UART5 TXO...</td>
<td>MCSI1 AX...</td>
<td>MCCI0 POW...</td>
<td>MCCI0 AX...</td>
</tr>
<tr>
<td>1</td>
<td>IDIS PD</td>
<td>M17 -</td>
<td>VDDS:5V=3.3V</td>
<td>MIDU DATA</td>
<td>TIMERS MUX2</td>
<td>UART5 RXD...</td>
<td>UART3 CTS...</td>
<td>MCCI0 SDC...</td>
<td>MCCI1 CMD...</td>
<td>MCCI2 CMD...</td>
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<td>1</td>
<td>IDIS PD</td>
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<td>MCCI2 CLK...</td>
</tr>
</tbody>
</table>
```

---

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Adding MMC to the U-Boot Board file

- Find the pre-processor flags in the `am335x_evrm.h` config file that control inclusion of MMC
- Use the name found for a weak alias to define in the board file
- Create the init function in the board file

```c
#define CONFIG_MMC
#define CONFIG_GENERIC_MMC
#define CONFIG_OMAP_HSMMC
#define CONFIG_CMD_MMC
#define CONFIG_DOS_PARTITION
#define CONFIG_CMD_FAT
#define CONFIG_CMD_EXT2
#endif

#define CONFIG_MMC

drivers/mmc/mmc.c

#include <config.h>
#include <am335xည culturally modified>

int board_mmc_init(bd_t *bd) __attribute__((weak, alias("__def_mmc_init")));

#include <config.h>
#include <am335xည culturally modified>

#define CONFIG_GENERIC_MMC
int board_mmc_init(bd_t *bd)
{
    omap_mmc_init(0);
    omap_mmc_init(1);
    return 0;
}
#endif

board/DocumentationBoardfile/evrm/c
```
Adding Ethernet to the U-Boot Board File

• Use the name found for a weak alias to define in the board file, in net/eth.c

• Create the init functions in the board file
  – 2 functions are created one to init the phy (local) and the board_eth_init
    definition for u-boot network driver to call

• There are additional supporting structures define in the board file

```c
/* CPU and board specific Ethernet initializations. Allocated function */
/* signals caller to move on */
static int __def_eth_init(bd_t *bis)
{
    return -1;
}

int board_eth_init(bd_t *bis) __attribute__((weak, alias("__def_eth_init")));

static void evm_phy_init(char *name, int addr)
{
    /* Large function.... */
}

int board_eth_init(bd_t *bis)
{
    eth_getenv_enetaddr();
    __raw_write(MII_MODE_ENABLE, MAC_MII_SEL);
    return cpsw_register(&cpsw_data);
}
```
git diff – Code Difference between template and mmc commit

• “git tag” is used to list tags on the git tree

• “git diff” this is used to isolate code between git commits.

```
schuylervorheus@/bp_uboot/sitara-board-port-uboot$ git diff 05.04.01.00-template..05.04.01.00-mmc
diff --git a/board/ti/am335x/evm.c b/board/ti/am335x/evm.c
index 1635871..b4d7e55 100644
--- a/board/ti/am335x/evm.c
+++ b/board/ti/am335x/evm.c
@@ -487,3 +487,15 @@ int board_late_init(void)
    return 0;
 }
+    #endif
+    +#ifndef CONFIG_SPL_BUILD
+    +#ifdef CONFIG_GENERIC_MMC
+    +int board_mmc_init(bd_t *bis)
+    +{
+    +    omap_mmc_init(0);
+    +    omap_mmc_init(1);
+    +    return 0;
+    +}
+    +#endif
+    +#endif /* CONFIG_SPL_BUILD */
```

• Do not be concerned about knowing git at this point, here we are using this for illustration purposes.
“git diff” commands goes across several screens

Type “q” to quit command at any point

Note the plus sign on the edge of the diagram, code addition
C code continuation for Ethernet PHY setup

This code was extracted from Beagle Bone specific code from the SDK release.
git diff – Code Difference between mmc and ethernet commit (cont)

• Code continuation for Ethernet setup

• This code was extracted from Beagle Bone specific code from the SDK release.

```c
+static struct cpsu_slave_data cpsu_slaves[] = {
+    {    
+        .slave_reg_offs = 0x208,
+        .slave_reg_offs = 0x0d00,
+        .phy_id = 0,
+    },
+    {    
+        .slave_reg_offs = 0x308,
+        .slave_reg_offs = 0x0d00,
+        .phy_id = 1,
+    },
+};

+static struct cpsu_platform_data cpsu_data = {
+    .ndio_base = AM335X_CPSU_MIII0_BASE,
+    .cpsu_base = AM335X_CPSU_BASE,
+    .ndio_div = 0xff,
+    .channels = 8,
+    .ndio_reg_offs = 0x0000,
+    .slaves = 2,
+    .slave_data = cpsu_slaves,
+    .ale_reg_offs = 0x0d00,
+    .ale_entries = 1024,
+    .host_port_reg_offs = 0x108,
+    .hw_stats_reg_offs = 0x0d00,
+    .mac_control = (1 << 5) /* MIIEN */,
+    .control = cpsu_control,
+    .phy_init = evm_phy_init,
+    .gigabit_en = 1,
+    .host_port_num = 0,
+    .version = CPSU_CTRL_VERSION_2,
+};

+int board_eth_init(bd_t *bd)
+{
+    uint8_t mac_addr[6];
+    :
```
git diff – Code Difference between mmc and ethernet commit (cont)

• Code continuation for Ethernet setup

• This code was extracted from Beagle Bone specific code from the SDK release.

• How is board_eth_init(..) called?

```c
+ .version = CPSU_CTRL_VERSION_2;
+);
+
+int board_eth_init(bd_t *bio)
+{
+    uint8_t mac_addr[6];
+    uint32_t mac_hi, mac_lo;
+    u_int32_t t;
+    
+    if (!eth_getenv_enetaddr("ethaddr", mac_addr)) {
+        debug("ethaddr not set. Reading from E-fuse\n");
+        /* try reading mac address from eFuse */
+        mac_lo = __raw_read((MAC_ID_LO);
+        mac_hi = __raw_read((MAC_ID_HI);
+        mac_addr[0] = mac_hi & 0xFF;
+        mac_addr[1] = (mac_hi & 0xFFFF) >> 8;
+        mac_addr[2] = (mac_hi & 0xFFFF0000) >> 16;
+        mac_addr[3] = (mac_hi & 0xFFFFFFFF) >> 24;
+        mac_addr[4] = mac_lo & 0xFF;
+        mac_addr[5] = (mac_lo & 0xFF000000) >> 24;
+        if (is_valid_ethernet(mac_addr))
+            eth_setenv_enetaddr("ethaddr", mac_addr);
+        else {
+            print("Caution: Using hardcoded mac address, ",
+                "Set <ethaddr> variable to overcome this,\n");
+        }
+    }
+    
+    __raw_write(HI_MODE_ENABLE, MAC_MII_SEL);
+    /* No gigabit */
+    cpsu_data.gigabit_on = 0;
+    return cpsu_register(&cpsu_data);
+}
```
Do LAB 2.........
U-Boot Board Port Summary

- Introduced a board port template file with a minimal feature set. Discussed the components in this file. This file could be used for actual board ports.

- Performed two labs demonstrating the template file in action.
Porting the Linux Kernel to a AM335x Target
Linux Port Agenda

- What are the different stages of a Port
- Introduce the board file, where it fits in the Port Picture, where it is in the source tree
- Discuss the OMAP2+ Machine Shared Common Code
- Labs Introduction
Linux Board Port Exercises and Source Links

• Link to the U-Boot Labs

• Link to the Linux Template Source tree (clone this tree)
  – git://gitorious.org/sitara-board-port/sitara-board-port-linux.git

• PSP Linux Kernel Repo –
  – [http://arago-project.org/git/projects/?p=linux-am33x.git;a=summary](http://arago-project.org/git/projects/?p=linux-am33x.git;a=summary)
Linux Kernel Overview (AHHHHH…. The Kernel…)

• A very complex and overwhelming kernel block diagram, this is just to make you aware of what’s below the waterline…..

• With a target port the architecture and SOC port has already been done. Therefore, the majority of this block diagram has been taken care of for the target port developer. Source is: http://en.wikipedia.org/wiki/File:Linux_kernel_map.png
Board Developers only need to be looking at the last phase which is board porting, all the architecture and SOC port support has been done.
The Target Port Starts with a Board File

- Defines the Machine Name
- Declares Initialization Data for Peripherals being used
- Declare Pin Mux initialization Data
- Defines Initialization functions
- Provides required Machine Initialization functions
- Calls Common Initialization functions
- Summary is that this file defines several required elements required to boot a Linux kernel, one of several bricks in the wall so to speak.
Linux Kernel Source Tree Overview
(Where is the Board file.)

• The board file is located in a source directory called arch/arm/mach-omap2/ where all other board files are located of the same machine type.
How the Board File fits in the stack

- Board Developer will spend most of their time in the Board file.
- The Board file makes use of the machine shared common code.
- The underlying port to the ARM Architecture Shared common code is already done and does not need to be looked at.
- Finally everything rests on the Linux Kernel Shared Common Code.
- The lower in the stack you go the less direct interaction the board developer will or need to have.
OMAP2+ Machine Shared Common Code

- There are several board files in the mach-omap2 directory. These board files typically use the support functions defined within this directory. Below is a sampling of some of the supporting common code, not all are mentioned here.

**OMAP2 Machine Shared Common Code — arch/arm/mach-omap2**

Not a complete listing of the interfaces, just a few are highlighted and to explain how they are used, review this directory to see additional interfaces.

- **serial**
  - Sets up UARTs including pin mux

- **devices**
  - Init calls, platform registration for most peripherals

- **common**
  - Init calls to define global address range for select interfaces

- **clocks**
  - Define clock domain mgmt functions

- **control**
  - OMAP2 control registers

- **display**
  - Display init calls, handles the differences between OMAP2,3 and 4

- **gpio**
  - Initialization function

- **i2c**
  - Reset and Mux functions

- **mux**
  - Defines a Pin Mux abstraction with supporting functions

- **hs_mmc**
  - Init functions, hw and platform data

- **sdrc**
  - Init function for SDRC and SMS

- **voltage**
  - Voltage domain support functions
OMAP2+ Machine Shared Common Code

• Provided as means to provide a common interface to the SOC peripherals to reduce the time necessary to implement a board port.

• This interface is not always a clear dividing between maintainers and board developers.

• This is not a documented interface and due to the changing nature of the Linux kernel will almost always be in flux. Maintainers in the end have the authority to accept reject code for their particular tree.
Linux Board Port Labs
Board Port Labs

• Lab 1
  – Introduce the template board file

• Lab 2
  – Build on the template file demonstrating how to add the MMC peripheral to provide a Root file system

• Lab 3
  – Build onto template file again this time adding Ethernet for network connectivity

• Lab 4
  – Demonstrate how to add an LCD panel to the board file
Board Port Source Tree being used

- Currently Source is derived from AM-SDK-05.04.01.00, the Port Tree will follow or track each SDK release.
- A git tree has been setup for these labs on the host machines.
- Using existing board file name and build methods.
- Using the default kernel configuration supplied with the SDK.

Lab Git Tree tag progression tags are SDK <version>_<modification>

- Base
  * board.am335xevmc
- Delete Board File
  * board.am335xevmc
- Create Template Board file
  * board.am335xevmc
  * 05.04.01.00-template
- MMC enable
  * 05.04.01.00-mmc
- Ethernet enable
  * 05.04.01.00-ethernet
- LCD Enable
  * 05.04.01.00-lcd
Linux Board Port Exercise 1 - Overview

• Goal: Introduce workshop attendees to a board template file that can be used later for a Linux board port

• How this is Demonstrated
  – Build a kernel using provided AM335x board template file, which has:
    • Base processor configuration for Linux, just serial console peripheral is initialized
    • This board will not completely boot… no peripheral is defined for a Root File System

• What is being done:
  – Examine the board file to see what is being initialized

• Perform the Lab
Template Board File Anatomy

- Binds Linux to a particular target
- Interfaces with the OMAP2+ Machine Shared Common Code.
- Defines pin mux configuration
- The file contains device initialization functions and data.
- Defines the Machine ID and identifies to the Linux Kernel initialization functions
**Template Board File Elements**

**MACHINE_START – Key Interface To Kernel**

- The Machine Start Macro is used to identify initialization functions to the Linux kernel.

- The `am335x_evm_map_io` is declared locally in the board file.

- The `am335x` is defined in the board file but calls common code to initialize the abstractions for the L3/L4 registers, this is existing code from the OMAP2+ Shared Common Code, no need to modify.

```c
MACHINE_START(AM335XEVM, "am335xevm")
    /* Maintainer: Texas Instruments */
    .tag_offset = 0x100,
    .map_io = am335x_evm_map_io,
    .init_early = am335x_init_early,
    .init_irq = ti01xx_init_irq,
    .handle_irq = omap3_intc_handle_irq,
    .timer = &omap3_am33xx_timer,
    .init_machine = am335x_evm_init,
MACHINE_END
```

Boot Parameter Location (these are passed from u-boot)

```c
static void __init am335x_evm_map_io(void)
{
    omap2_set_globals_am33xx();
    omapam33xx_map_common_io();
}
```

Arch/arm/mach-omap2/board-am335xevm.c

OMAP2+ Machine Shared Common Code

```c
common.c - omap2_set_globals_am33xx()
Registers the physical address for the:
- Control Module
- SDRAM Controller
- System Control Module
- Power and Reset Management
- Clock Management

ip.c - omapam33xx_map_common_io()
Registers the physical address for the:
- L3 and L4 address range
```
The am33xx_init_early is a function within the OMAP2+ Shared common code.

This is called directly from the common code without modification.
• All three of these functions defined come from the OMAP2+ Shared Common Code, none of these needed to be modified.
The am335x_evm_init() is defined by the developer, but uses several functions from the OMAP2 Common Code without modification.
Question

Within the kernel source, where is the am335xevm board file located?

arch/arm/mach-omap2
Do LAB 1........
Linux Board Port Exercise 2 - Overview

• Goal: Build on the template file demonstrating how to add the MMC peripheral to provide a Root file system

• How this is demonstrated:
  – Using the provided lab git tree branch that has the code additions necessary to enable MMC
  – With MMC enabled the root file system can now be mounted

• What is being done:
  – Explaining the code addition components

• Perform the Lab
Steps to adding an MMC interface to target board file

- Review system info to see how peripheral is attached
- Pin Mux
  - Use the Pin Mux Utility to configure Pin Init data
- Device/Platform Initialization data
  - Some peripherals may not require init data
- Create Device Init function
- Add Device Init function to EVM Init Function
How is the peripheral attached? – Schematic to Pin Mux Utility

- Beagle Bone Schematic

- Pin Mux Tool Capture

- Beagle Bone does not use the WP pin

Using the pin mux tool to isolate the pin necessary for mmc0

<table>
<thead>
<tr>
<th>Mode 0</th>
<th>Mode 1</th>
<th>Mode 2</th>
<th>Mode 3</th>
<th>Mode 4</th>
<th>Mode 5</th>
<th>Mode 6</th>
<th>Mode 7</th>
</tr>
</thead>
<tbody>
<tr>
<td>MMC0_DAT3</td>
<td>GPMC_A20...</td>
<td>UART4_CTS...</td>
<td>TIMER5_MUX0</td>
<td>UART1_DCD...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:26]</td>
</tr>
<tr>
<td>MMC0_DAT2</td>
<td>GPMC_A2...</td>
<td>UART4_RTS...</td>
<td>TIMER6_MUX0</td>
<td>UART1_DSR...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:27]</td>
</tr>
<tr>
<td>MMC0_DAT1</td>
<td>GPMC_A2...</td>
<td>UART5_CTS...</td>
<td>UART3_RXD...</td>
<td>UART1_DTR...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:26]</td>
</tr>
<tr>
<td>MMC0_DAT0</td>
<td>GPMC_A2...</td>
<td>UART5_RTS...</td>
<td>UART3_TXD...</td>
<td>UART1_RIN...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:26]</td>
</tr>
<tr>
<td>MMC0_CLK</td>
<td>GPMC_A2...</td>
<td>UART5_CTS...</td>
<td>UART2_RXD...</td>
<td>DCAM1_TX...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:01]</td>
</tr>
<tr>
<td>MMC0_CMD</td>
<td>GPMC_A2...</td>
<td>UART5_RTS...</td>
<td>UART2_TXD...</td>
<td>DCAM1_RX...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[02:01]</td>
</tr>
<tr>
<td>SPI0_CS1</td>
<td>UART3_RXD...</td>
<td>ECAPI_IN...</td>
<td>MMC0_POW...</td>
<td>XDMA_EVT...</td>
<td>MMC0_SDC...</td>
<td>ENH4_MUX1</td>
<td>GP[00:61]</td>
</tr>
<tr>
<td>MCAST0 AC...</td>
<td>EDPDA_IN...</td>
<td>MCAST0 AX...</td>
<td>MCAST1 AC...</td>
<td>MMC0_SDW...</td>
<td>PR1_PRU0...</td>
<td>PR1_PRU0...</td>
<td>GP[03:16]</td>
</tr>
</tbody>
</table>

Have simplified the pin mux tool to show the pins necessary for the mmc0 interface

MMC0 pins for data, clk, cmd are being used from mode 0

GPIO 0 & 6 and GPIO 3 & 18 are being used for card detect and write protect respectively mode 7

Texas Instruments
Lab 2 Board File Additions – Pin Mux Initialization Data

• Capture from the Pin Mux tool, AM3358 ZCZ package

• Use existing pinmux_config struct to create pin mux initialization data for mmc0

• Number of pins has to match

Using the pin mux tool to isolate the pin necessary for mmc0

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<td>UART4 CTSN</td>
<td>TIMERS MUX0</td>
<td>UART1 DCD</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[26]</td>
</tr>
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<td>MMC0 DAT2</td>
<td>GPICL_A2</td>
<td>UART4 RTSN</td>
<td>TIMERS MUX0</td>
<td>UART1 DSR</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[27]</td>
</tr>
<tr>
<td>MMC0 DAT1</td>
<td>GPICL_A22</td>
<td>UART5 CTSN</td>
<td>UART3 RXD</td>
<td>UART1 DTR</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[26]</td>
</tr>
<tr>
<td>MMC0 DAT0</td>
<td>GPICL_A23</td>
<td>UART5 RTSN</td>
<td>UART3 TXD</td>
<td>UART1 RIN</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[29]</td>
</tr>
<tr>
<td>MMC0 CLK</td>
<td>GPICL_A24</td>
<td>UART3 CTSN</td>
<td>UART2 RXD</td>
<td>DCMAN TX</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[30]</td>
</tr>
<tr>
<td>MMC0 CMD</td>
<td>GPICL_A25</td>
<td>UART3 RTSN</td>
<td>UART2 TXD</td>
<td>DCMAN RX</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO2[31]</td>
</tr>
<tr>
<td>SPI0 CS1</td>
<td>UART3 RXD</td>
<td>ECPA1 IN F</td>
<td>MMC0 POW</td>
<td>XDMA EVE</td>
<td>MMC0 SDC</td>
<td>ENUL4 MIX1</td>
<td>GPIO0[6]</td>
</tr>
<tr>
<td>HCASPO AC</td>
<td>ECPA0IN</td>
<td>HCASPO AX</td>
<td>MMC0 SDA</td>
<td>MMC0 SDW</td>
<td>PR1 PRU0</td>
<td>PR1 PRU0</td>
<td>GPIO0[10]</td>
</tr>
</tbody>
</table>

Have simplified the pin mux tool to show the pins necessary for the mmc0 interface
MMC0 pins for data, clk, cmd are being used from mode 0
GPIO 0.6 and GPIO 3.18 are being used for card detect and write protect respectively mode 7

Pin Mux definition for MMC0

```c
/* Module pin mux for mmc0 */
static struct pinmux_config mmc0_pin_mux[] = {
    {"mmc0_dat3.mmc0_dat3", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mmc0_dat2.mmc0_dat2", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mmc0_dat1.mmc0_dat1", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mmc0_dat0.mmc0_dat0", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mmc0_clk.mmc0_clk", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mmc0_cmd.mmc0_cmd", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"mcasp0_clk.mmc0_clk", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {"spi0_cs1.mmc0_dat0", OMAP_MUX_NMODE0 | AM33XX_PIN_INPUT_PULLUP},
    {null, 0},
};
```

\[\text{pin name - mmc0_dat3} \quad \text{pin value and type} \quad \text{arch/arm/mach-omap2/mux33xx.c} \quad \text{arch/arm/mach-omap2/mux.h}\]

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Lab 2 Board File Additions – MMC Device Initialization Data

OMAP 2 mmc structure
definition

Only the elements used are shown, several more

MMC initialization structure to enable interface #1

This init data is from EVM, BB does not use WP signal

`*/ Convert GPIO signal to GPIO pin number */`
`#define GPIO_TO_PIN(bank, gpio) (32 * (bank) + (gpio))`

```c
static struct omap2_hsmmc_info am335x_mmc[] __initdata = { {
    .mmc = 1,
    .caps = MMC_CAP_4_BIT_DATA,
    .gpio_cd = GPIO_TO_PIN(0, 6),
    .gpio_wp = GPIO_TO_PIN(3, 10),
    .ocr_mask = MMC_VDD_32_33 | MMC_VDD_33_34, /* 3V3 */
},
{
    .mmc = 0, /* will be set at runtime */
},
{
    .mmc = 0, /* will be set at runtime */
},
} /* Terminator */
```

```c
struct omap2_hsmmc_info {
    u8   mmc;   /* controller 1/2/3 */
    u32  caps;  /* 4/8 wires and any additional host * capabilities OR’d (ref. linux/mmc/host.h) */
    ... int   gpio_cd; /* or -EINVAL */   <Card Detect>
    int   gpio_wp; /* or -EINVAL */   <Write Protect>
    ... int   ocr_mask; /* temporary HACK */ <voltage range for slot>

    ...
};
```

arch/arm/mach omap2/hsmmc.h – omap2_hsmmc_info
include/linux/mmc/host.h – capabilities definitions and voltage range definitions
Initialization Function Call Sequence for MMC Enabling

• This sequence of code is adding in the MMC initialization code to the template file.

```c
static void __init am335x_evm_init(void) {
    am33xx_cpuidle_init();
    am33xx_mux_init(NULL);
    omap_serial_init();
    am335x_rtc_init();
    clkout2_enable();
    omap_sdrc_init(NULL, NULL);
}

/* BeagleBone has Micro SD slot which doesn't have Write Protect pin */
am335x_mmc[0].gpio_wp = -EINVAL;
mmc0_init();
```

Registers MMC init data with Linux
mmc0 initialization – did it work?

Did mmc0 messages show up in the console log or dmesg log?

```
[ 1.078450] mmc0: host does not support reading read-only switch, assuming write-enable.
[ 1.083355] mmc0: new high speed SDHC card at address 1234
[ 1.096754] mmcblk0: mmc0:125d SD46 1.08 AR
[ 1.102742] mmcblk0p0: p0 p2
```

Did mmc0 show up in sysfs?

```
root@am335x-eval:~# ls -la /sys/devices/platform/omap/omap_hsmmc,0/
driver-xr-x 4 root root 0 Dec 28 03:45 ...
driver-xr-x 23 root root 0 Dec 28 03:45 ...
lnwxrwxrwx 1 root root 0 Dec 28 03:45 driver -> ../../bus/platform/drivers/omap_hsmmc
lnwxrwxrwx 1 root root 0 Dec 28 03:45 subsystem -> ../../bus/platform
```

Just for curiosity sake... did the root file system mount to mmc?

```
root@am335x-eval:~# mount
rootfs on / type rootfs (ru) /dev/root on / type ext3 (ru,relative,errors=continue,barrier=1,data=ordered) proc on /proc type proc (ru,relative) tmpfs on /mnt/splash type tmpfs (ru,relative,size=40K) sysfs on /sys type sysfs (ru,relative) none on /dev type tmpfs (ru,relative,size=1024K,mnt_opt=5192,mode=755) /dev/mmcblk0p0 on /media/mmcblk0p0 type ext3 (ru,relative,errors=continue,barrier=1,data=ordered) /dev/mmcblk0p1 on /media/mmcblk0p1 type vfat (ru,relative,fmask=0022,dmask=0022,codepage=cp437,iocharset=iso8859-1,shortname=mixed,errors=remount-ro) devpts on /dev/pts type devpts (ru,relative,gid=5,mode=620) usbfs on /proc/bus/usb type usbfs (ru,relative) tmpfs on /var/volatile type tmpfs (ru,relative,mode=65540) tmpfs on /var/run type tmpfs (ru,relative,mode=65540) tmpfs on /dev/shm type tmpfs (ru,relative,mode=777)
```

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git diff – Code Difference between template and mmc commit

• Code for MMC setup

• This code was extracted from Beagle Bone specific code from the SDK release.

• git tag result for linux board port tree

• git diff command for this commit
git diff – Code Difference between template and mmc commit (cont)

• Code for MMC setup
• Pin mux was started on previous page
• This code was extracted from Beagle Bone specific code from the SDK release.

```c
+ {"mmc0_dat3,mmc0_dat3", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_dat2,mmc0_dat2", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_dat1,mmc0_dat1", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_dat0,mmc0_dat0", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_clk,mmc0_clk", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_sd0,mmc0_sd0", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_sd1,mmc0_sd1", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_sd2,mmc0_sd2", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"mmc0_sd3,mmc0_sd3", OMAP_MUX_MODE0 | AM335X_PIN_INPUT_PULLUP},
+ {"sp0.cs1,mmc0_sd4", OMAP_MUX_MODE7 | AM335X_PIN_INPUT_PULLUP},
+ {NULL, 0},
+
+ /*
+ * @pin_mux - single module pin-mux structure which defines pin-mux
+ * details for all its pins.
+ * -233.6 +124.14 @ static struct pinmux_config clkout2_pin_mux[] = {
+ * {NULL, 0},
+ *
+ static void mmc0_init(void)
+
+ * {
+ * setup_pin_mux(mmc0_pin_mux);
+ * omap2_hsmmc_init(am335x_mmc);
+ * return;
+ *
+ *
+ static void __init clkout2_enable(void)
+
+ * {
+ * struct clk *clk_32;
+ * -233.6 +273.10 @ static void __init am335x_evm_init(void)
+ * clkout2_enable();
+ * omap_sdr0_init(NULL, NULL);
+ */ Beagle Bone has Micro-SD slot which doesn't have write Protect pin */
+ * am335x_mmc[0].gpio_wp = -EINVAL;
+ * mmc0_init();
+ */
```
git diff – Code Difference between template and mmc commit (cont)

- Code for MMC setup
- Note this looks like a repeat from previous page, only these lines are different...
- How is mmc0_init() called?
- This code was extracted from Beagle Bone specific code from the SDK release.
- use “q” to quit
Do LAB 2........
Lab 2 Summary

- Added code to the board port template file to handle pin mux, MMC controller initialization and evm initialization function.
- All changes happened within the board file
Linux Board Port Exercise 3 - Overview

• Goal: Build onto the template file again adding Ethernet for Network connectivity

• How this is demonstrated:
  – Using the lab git tree branch with the code additions necessary to enable Ethernet
  – With Ethernet enabled Remote Matrix will be brought up on the browser on the Host machine

• What is being done:
  – Explaining the code addition components (in multiple files this time)

• Perform the Lab
Steps to adding Ethernet to target board file

- Review system info to see how peripheral is attached
- Pin Mux
  - Use the Pin Mux Utility to configure Pin Init data
- Device/Platform Initialization data
  - None required for this integration
- Create Device Init function
  - Additional Init code required outside the board file
- Add Device Init Function to EVM Init Function
MII Ethernet connection

- Captured from Beagle Bone SRM
- Please use RMII for MII here in this example, doc bug....
- MII Interface signals that need to be indentified to the driver
Pin Mux Utility and pinmux config struct

- Pin Mux tool capture for MII interface
- While the tool shows GMII this is the MII interface, doc bug in tool

This demonstrates how the Pin Mux utility can assist in filling out the pinmux_config structure

```c
/* Module pin mux for mii */
static struct pinmux_config mii1_pin_mux[] = {
    "mii_rxerr.mii_rxerr", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txon.mii_txon", OMAP_MUXMODE0 | AM33XX_PIN_INPUT,
    "mii_rxdv.mii_rxdv", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv3.mii_txdv3", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv2.mii_txdv2", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv1.mii_txdv1", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv0.mii_txdv0", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txclk.mii_txclk", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txclk.mii_txclk", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv3.mii_txdv3", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv2.mii_txdv2", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv1.mii_txdv1", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mii_txdv0.mii_txdv0", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mdio_data.mdio_data", OMAP_MUXMODE0 | AM33XX_PIN_INPUT_PULLDOWN,
    "mdio_clk.mdio_clk", OMAP_MUXMODE0 | AM33XX_PIN_OUTPUT_PULLUP,
    {NULL, 0},
};
```
Reason - This is code added to devices.c to supplement existing am33x_cpsw_init, does not require eeprom support.

- Reads the MAC IDs
- Sets the PHY type
- Registers MDIO
- Registers CPSW with Linux kernel
Ethernet Device Init and EVM Init functions

- The MII init function – call pin mux setup.

```
static void mii1_init(void)
{
    setup_pin_mux(mii1_pin_mux);
    return;
}
```

EVM Init function (simplified for discussion purposes, just the added part for Ethernet)

```
/* Called as part of board initialization, defined in MACHINE_START */
static void __init am335x_evm_init(void)
{
    mii1_init();
    am335x_cpsw_init_generic(MII_MODE_ENABLE,gigabit_enable);
}
```

- The EVM init function – calls mii1_init and the cpsw init function.
Ethernet Initialization – Did it work?

Did the PHY message show in the console or dmesg log?

```
[ 12.288146] [ 12.288177] CPSM phy found : id is : 0x7c:f1
[ 12.294321] PHY 0:01 not found
eth0     no wireless extensions.
udhcpc (v1.13.2) started
Sending discover...
[ 15.203547] PHY; 0:00 - Link is Up - 1000-null
Sending discover...
Sending select for 128.247.107.4...
Lease of 128.247.107.4 obtained, lease time 28800
adding dns 128.247.5.10
adding dns 197.170.147.7
```

Was an IP address obtained?

```
root@am335x-evm:~# ifconfig -a
eth0     Link encap:Ethernet  HWaddr 40:5F:C2:76:36:16
         inet addr:128.247.107.4  Bcast:0.0.0.0  Mask:255.255.254.0
         UP BROADCAST RUNNING ALLMULTI  MTU:1500
         RX packets:14495  errors:0  dropped:5377  overruns:0  frame:0
         TX packets:7  errors:0  dropped:0  overruns:0  carrier:0
         collisions:0  txqueuelen:1000
         RX bytes:1539755 (1.4 KiB)  TX bytes:1100 (1.1 KiB)
         Interrupt:40

lo       Link encap:Local Loopback
         inet addr:127.0.0.1  Bcast:0.0.0.0  Mask:255.0.0.0
         UP LOOPBACK RUNNING MTU:65536 Metric:1
         RX packets:0  errors:0  dropped:0  overruns:0  frame:0
         TX packets:0  errors:0  dropped:0  overruns:0  carrier:0
         collisions:0  txqueuelen:0
         RX bytes:0 (0.0 B)  TX bytes:0 (0.0 B)
```

Can ping another machine on the network?

```
root@am335x-evm:~# ping 128.247.106.201
PING 128.247.106.201 (128.247.106.201) 56 data bytes
64 bytes from 128.247.106.201: seq=0 ttl=54 time=0.383 ms
64 bytes from 128.247.106.201: seq=1 ttl=54 time=0.244 ms
64 bytes from 128.247.106.201: seq=2 ttl=54 time=0.714 ms
64 bytes from 128.247.106.201: seq=3 ttl=54 time=0.183 ms
64 bytes from 128.247.106.201: seq=4 ttl=54 time=0.275 ms
```
Do LAB 3.........
Lab 3 summary

• Followed the steps of system attach review, pin mux config, device init to evm init

• Had to add additional code outside the board file to support initializing the cpsw for a generic case
Linux Board Port Exercise 4 - Overview

• Goal: Build onto the template file again adding support for an LCD panel

• How this is demonstrated:
  – Using the lab git tree tagged branch with code additions necessary to enable an LCD Panel

• What is being done:
  – Explaining the code addition components (multiple files this time)

• Perform the Lab
Steps to adding an LCD Panel to target board file

• Review the system
  – 3 interfaces used: PWM (backlight), LCD, Touch Screen

• Pin Mux
  – Use the Pin Mux Utility to configure Pin Init data

• Device/Platform Initialization data?
  – Backlight, LCD and Touch screen all have initialization data

• Create Device Init function initializes all 3 components

• Add Device init to board_init
LCD Panel Functional Components

- LCD is the same 7" panel currently found on the EVM
- The respective controllers require data initialization
• Pin Mux Tool capture for the LCD Panel
LCD Touch Screen Pin Mux Initialization

- Pin Mux Capture of Pins used for Touch Screen
- 4 Wire Resistive touch
- 2 Wire for Voltage reference
- Pin connections are determined by schematic reference

```c
/* Module pin mux for touchscreen controller */
static struct pinmux_config tsc_pin_mux[] = {
    {"ain0.ain0", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {"ain1.ain1", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {"ain2.ain2", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {"ain3.ain3", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {"vrefp.vrefp", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {"vrefn.vrefn", OMAP_MUX_MODE0 | AM33XX_INPUT_EN},
    {NULL, 0},
};
```
LCD Back Light Pin Mux Initialization

- Just a single pin used for the backlight.
- This is a pwm signal that is used to control brightness

```c
/* Module pin mux for LCD backlight */
static struct pinmux_config ehrpwm_pin_mux[] = {
    {"gpmc_a2.ehrpwm1A", OMAP_MUX_MODE6 | AM33X_PIN_OUTPUT},
    {NULL, 0},
};
```
Add LCD Panel – Data Initialization

This configures the registers in the LCD Controller.

The datasheet for LCD will provide information (to name a few)
  - BPP
  - Clock polarity
  - Data Format
  - DMA

```c
/* Define display configuration */
static struct lcd_ctrl_config bhcape7_cfg = {
  &bhcape7_panel,
  .ao_bias = 255,
  .ao_bias_intrpt = 0,
  .dma_burst_sz = 16,
  .bpp = 16,
  .fdd = 0x80,
  .tft_all_mode = 0,
  .str_565_mode = 0,
  .mono_0bit_mode = 0,
  .invert_line_clock = 1,
  .invert_frm_clock = 1,
  .sync_edge = 0,
  .sync_ctrl = 1,
  .raster_order = 0,
};

struct lcd_ctrl_config {
  const struct display_panel *p_disp_panel;
  /* AC Bias Pin Frequency */
  int ac_bias;
  /* AC Bias Pin Transitions per Interrupt */
  int ac_bias_intrpt;
  /* DMA Burst size */
  int dma_burst_sz;
  /* Bits per pixel */
  int bpp;
  /* FIFO DMA Request Delay */
  int fdd;
  /* TTY Alternative Signal Mapping (Only for active) */
  unsigned char tft_all_mode;
  /* 12 Bit Per Pixel (5-6-5) Mode (Only for passive) */
  unsigned char str_565_mode;
  /* Mono 0-bit Mode: 1=D0-D7 or 0=D0-D3 */
  unsigned char mono_0bit_mode;
  /* Invert frame clock */
  unsigned char invert_line_clock;
  /* Invert frame clock */
  unsigned char invert_frm_clock;
  /* Horizontal and Vertical Sync Edge: 0-rising 1-falling */
  unsigned char sync_edge;
  /* Horizontal and Vertical Sync: Control: 0-ignore */
  unsigned char sync_ctl;
  /* Raster Data Order Select: 1=MSB-to-MSB 0=Least-to-Most */
  unsigned char raster_order;
  /* DMA FIFO threshold */
  int fifo_th;
};
```
LCD Panel Initialization data used by the LCDC

```c
/* ThreeFive S9700 RTWV35TR */
[2] = {
    .name = "TF3_S9700RTWV35TR_01B",
    .width = 800,
    .height = 480,
    .hfp = 39,
    .hbp = 39,
    .hs = 47,
    .vs = 13,
    .vhf = 29,
    .vsw = 2,
    .pxl_clk = 330000000,
    .invert_pxl_clk = 0,
},
```

- LCD Panel interfacing numbers have to be added in the da8xx-fb.c if they are not already defined.

- These numbers are derived from the datasheet for the panel (to name a few)
  - Screen resolution
  - Timings
  - Pixel Clock and Polarity
Backlight Initialization Data

- PWM is used to control the LCD Panel Brightness

```c
/* LCD backlight platform Data */
#define AM335X_BACKLIGHT_MAX_BRIGHTNESS 100
#define AM335X_BACKLIGHT_DEFAULT_BRIGHTNESS 50
#define AM335X_PWM_PERIOD_NANO_SECONDS (1000000 * 5)

/* Setup pwm-backlight for bbtoys7lcd */
static struct platform_device bbtoys7lcd_backlight = {
    .name = "pwm-backlight",
    .id = -1,
    .dev = {
        .platform_data = &bbcape7lcd_backlight_data,
    }
};

static struct platform_pwm_backlight_data bbcape7lcd_backlight_data = {
    .pwm_id = BBCAPE7LCD_PWM_DEVICE_ID,
    .ch = -1,
    .max_brightness = AM335X_BACKLIGHT_MAX_BRIGHTNESS,
    .dft_brightness = AM335X_BACKLIGHT_DEFAULT_BRIGHTNESS,
    .pwm_period_ns = AM335X_PWM_PERIOD_NANO_SECONDS,
};
```
LCD Init Function

• The steps are:
  – Pin mux setup
  – Assign a GPIO to support VDD_en to the LCD
    • Refer to schematic on which to use
  – Define PLL value for the pixel clock
  – Register with the kernel

```c
/* Configure display pll */
static int __init conf_disp_pll(int rate)
{
    struct clk *disp_pll;
    int ret = -EINVAL;

    disp_pll = clk_get(NULL, "dpll_disp_clk");
    if (!disp_pll) {
        pr_err("Cannot clk_get disp_pll\n");
        goto out;
    }

    ret = clk_set_rate(disp_pll, rate);
    clk_put(disp_pll);

    out:
    return ret;
}
```

```c
/* Initialize and register lcdc device */
#define BEAGLEBONE_LCD_AVDD_EN GPIO_TO_PIN(8, 7)

static void bbcape_lcd_init(void)
{
    setup_pinmux(bbcape7_pinmux);
    gpio_request(BEAGLEBONE_LCD_AVDD_EN, "BONE_LCD_AVDD_EN");
    gpio_direction_output(BEAGLEBONE_LCD_AVDD_EN, 1);

    if (!conf_disp_pll(300000000)) {
        pr_info("Failed to set pixel clock to 300000000, not attempting to"
                "register LCD cape\n");
        return;
    }

    if (amm335x_register_lcdc(&bbcape7_pdata))
        pr_info("Failed to register Beagleboard LCD cape device\n");
    return;
}
```
LCD Clocking Layout

\[ \text{LCD_PCLK} = \frac{\text{LCD_CLK}}{\text{CLKDIV}} \]
Touch Screen and Backlight Init Functions

- These init functions call the pin mux config function with the earlier defined initialized structures

```c
/* Initialize and register tsc device */
static void tsc_init(void)
{
    int err;

    am335x_touchscreen_data.analog_input = 1;
    setup_pin_mux(tsc_pin_mux);
    err = am335x_register_tsc(&am335x_touchscreen_data);
    if (err)
        pr_err("failed to register touchscreen device\n                ");
}

/* Enable ehrpwm for backlight control */
static void enable_ehrpwm1(void)
{
    ehrpwm_backlight_enable = true;
    setup_pin_mux(ehrpwm_pin_mux);
}
```
LCD Init Sequence in the EVM Init function

- Calling three functions, initialization of
  - Backlight
  - LCD
  - touchscreen

```c
/* Called as part of board initialization, defined in MACHINE_START */
static void __init am335x_evm_init(void)
{
    enable_ehrpwm1();
    bbcaps7lcd_init();
    tsc_init();
}
```
Do LAB 4.........
Summary Lab 4

• LCD required 3 functions to be configured, Backlight, Touch Screen and LCDC
  – required device initialization data
  – required init functions
  – required pin mux configurations

• Made additions to the board file and the frame buffer support file
So.... does it work yet?  Works Enough!
Thank You
Additional Information Sources for Post Workshop Review